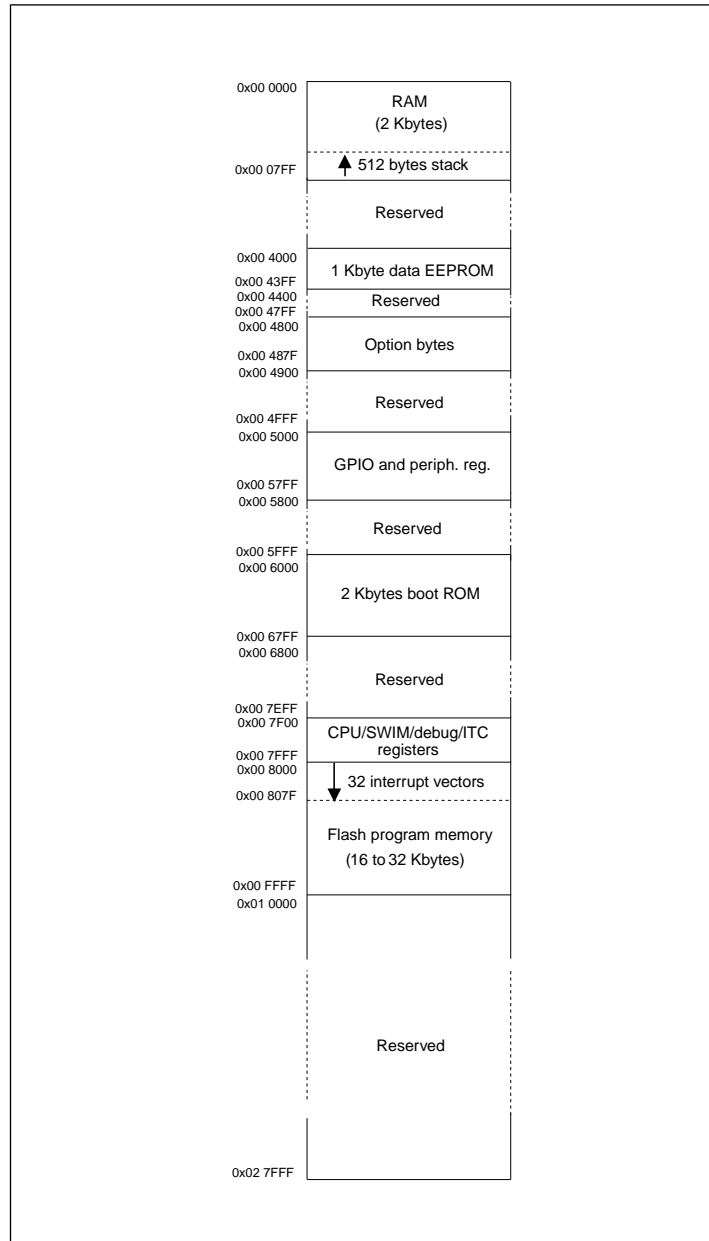


6 Memory and register map

6.1 Memory map

Figure 7: Memory map



The following table lists the boundary addresses for each memory size. The top of the stack is at the RAM end address in each case.